

Fig. 1 PRIOR ART

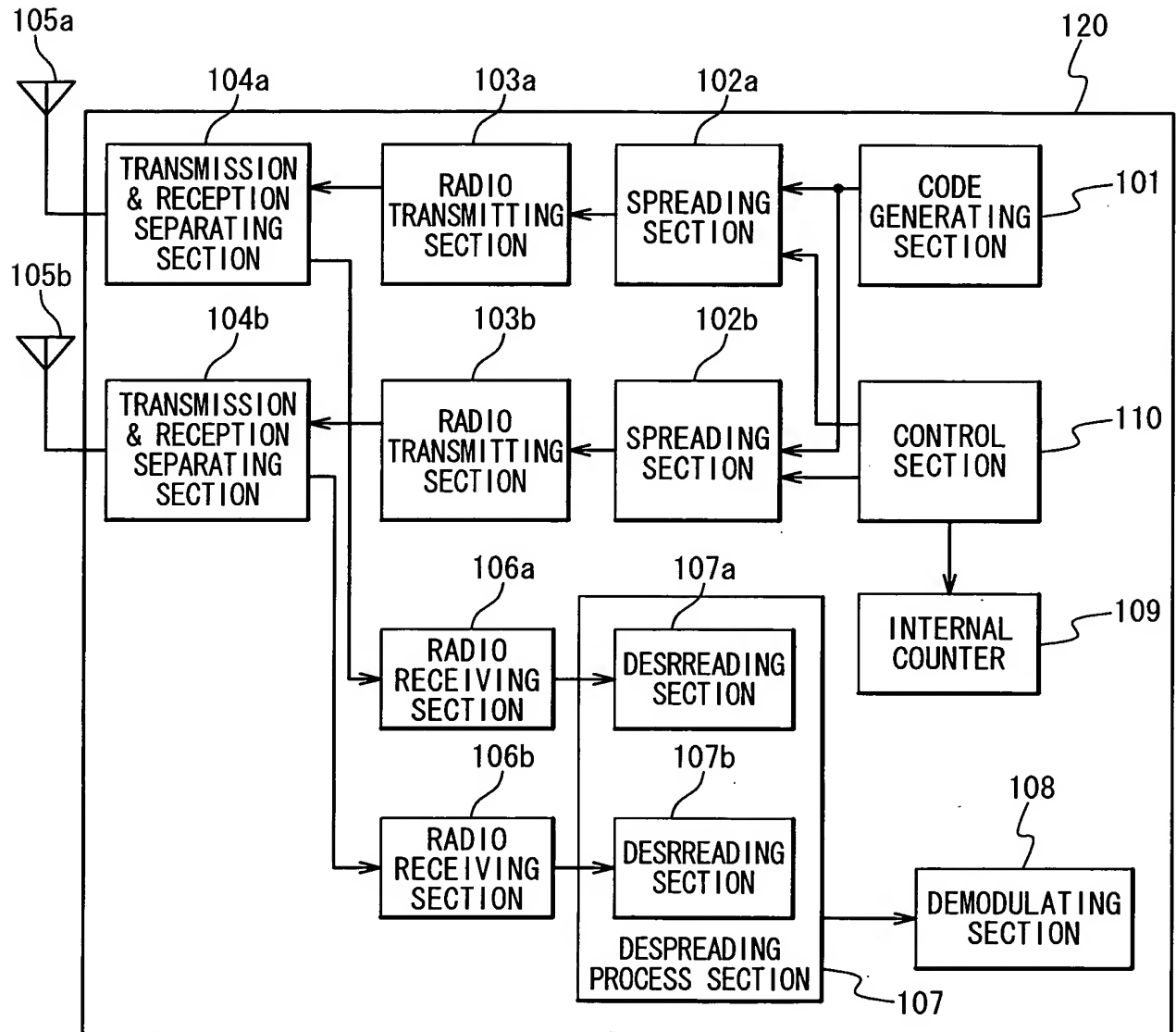


Fig. 2

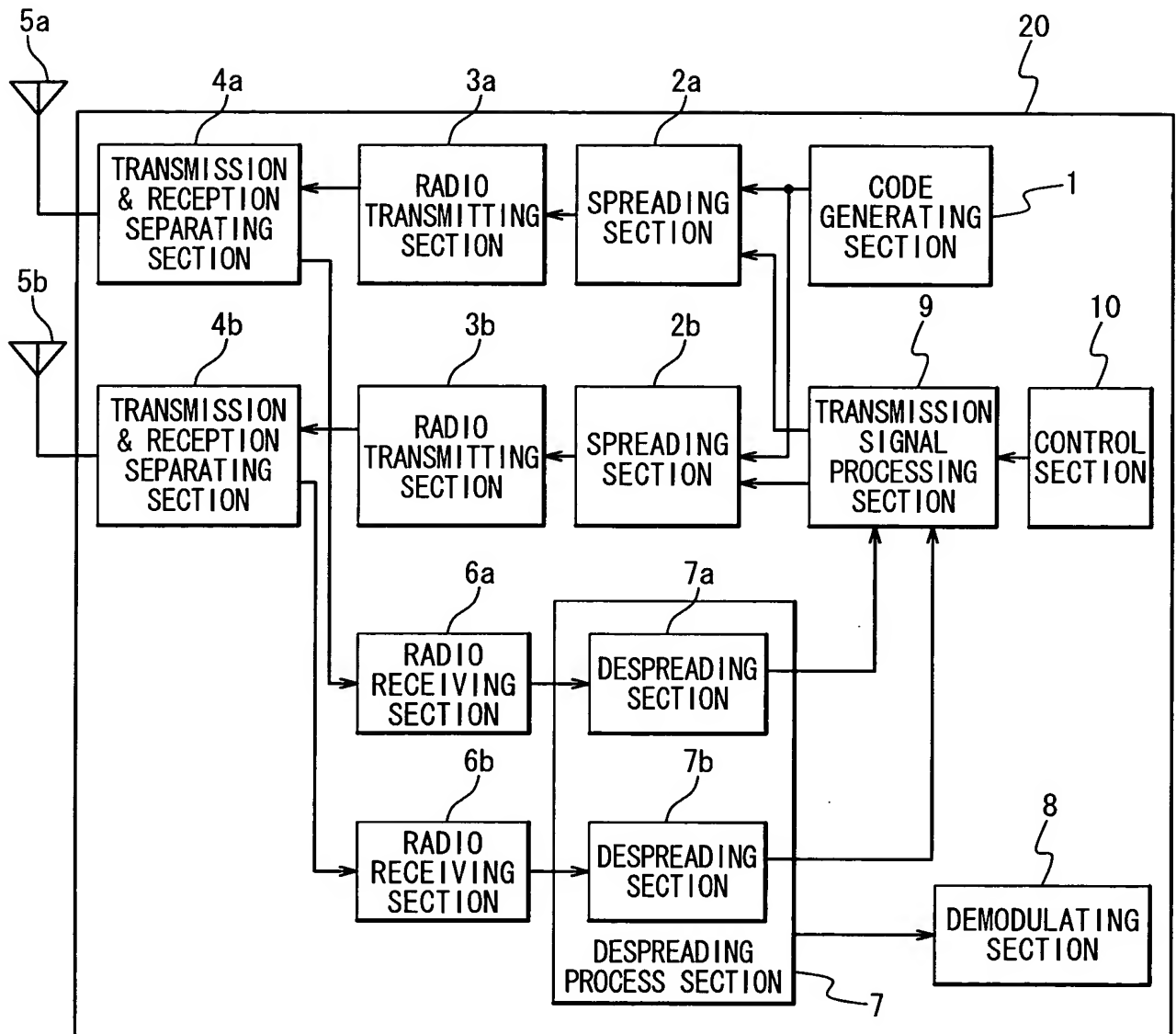


Fig. 3

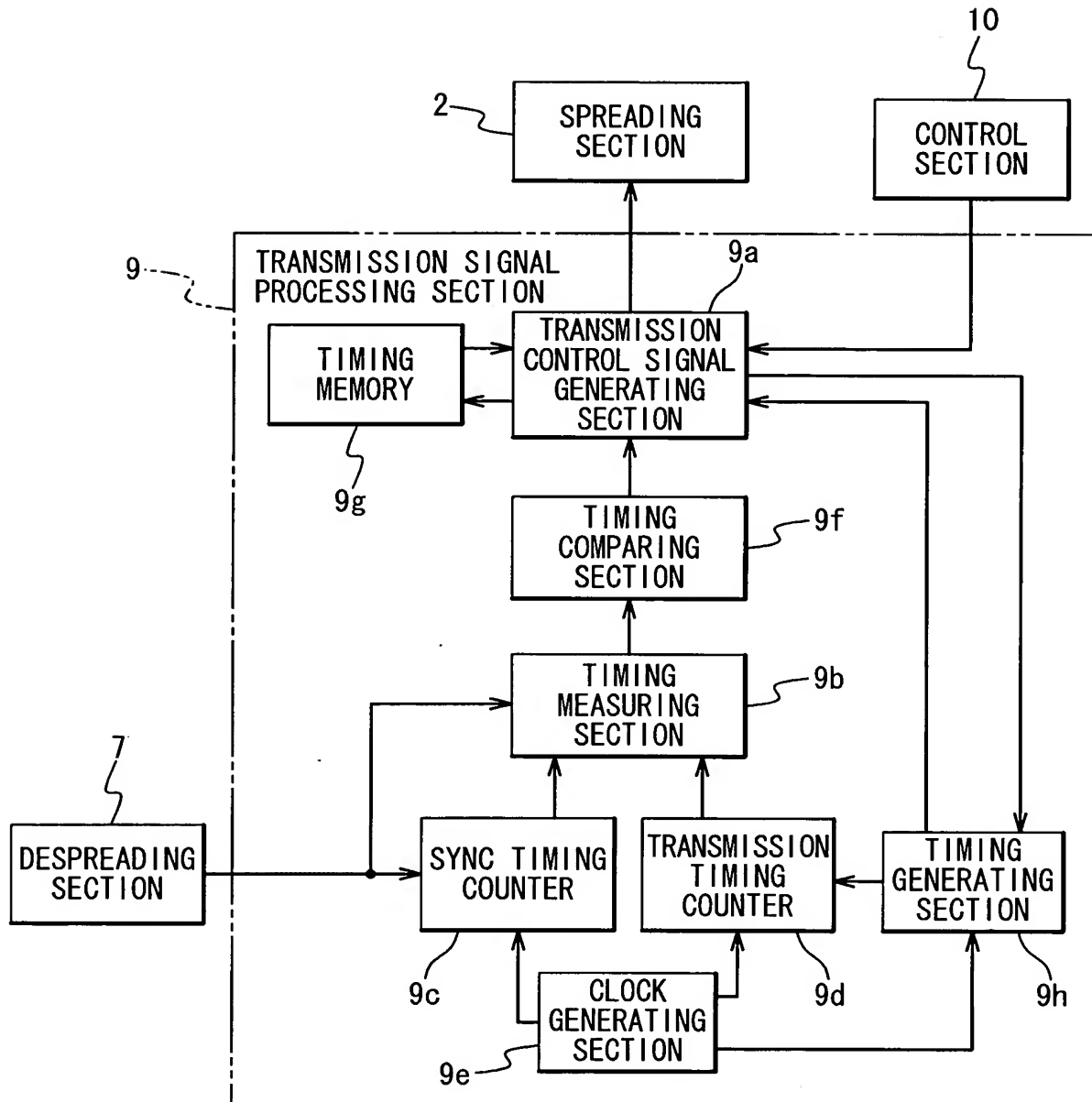


Fig. 4A

CLOCK SIGNAL CL



Fig. 4B

FIRST TRANSMISSION
START SIGNAL ST1

SIGNAL DETECTION

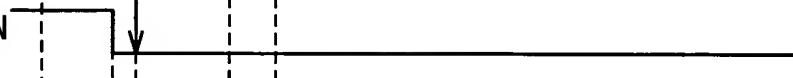


Fig. 4C

TRANSMISSION START
TIMING VALUE TM

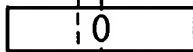


Fig. 4D

COUNTER VALUE C0

COUNTER SET, TIMING COINCIDENCE DETECTION



Fig. 4E

SECOND TRANSMISSION
START SIGNAL ST2

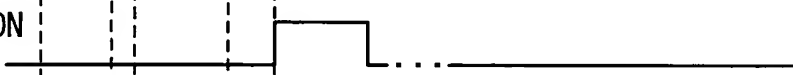


Fig. 4F

TRANSMISSION TIMING
COUNTER VALUE C1

LATCH TIMING



Fig. 4G

SYNC ESTABLISHING
SIGNAL SD

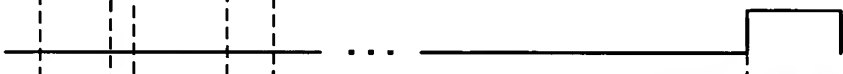
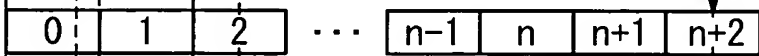


Fig. 4H

SYNCH TIMING
COUNTER VALUE C2

LATCH TIMING



t01 t02 t03 t04

t05

Fig. 5A

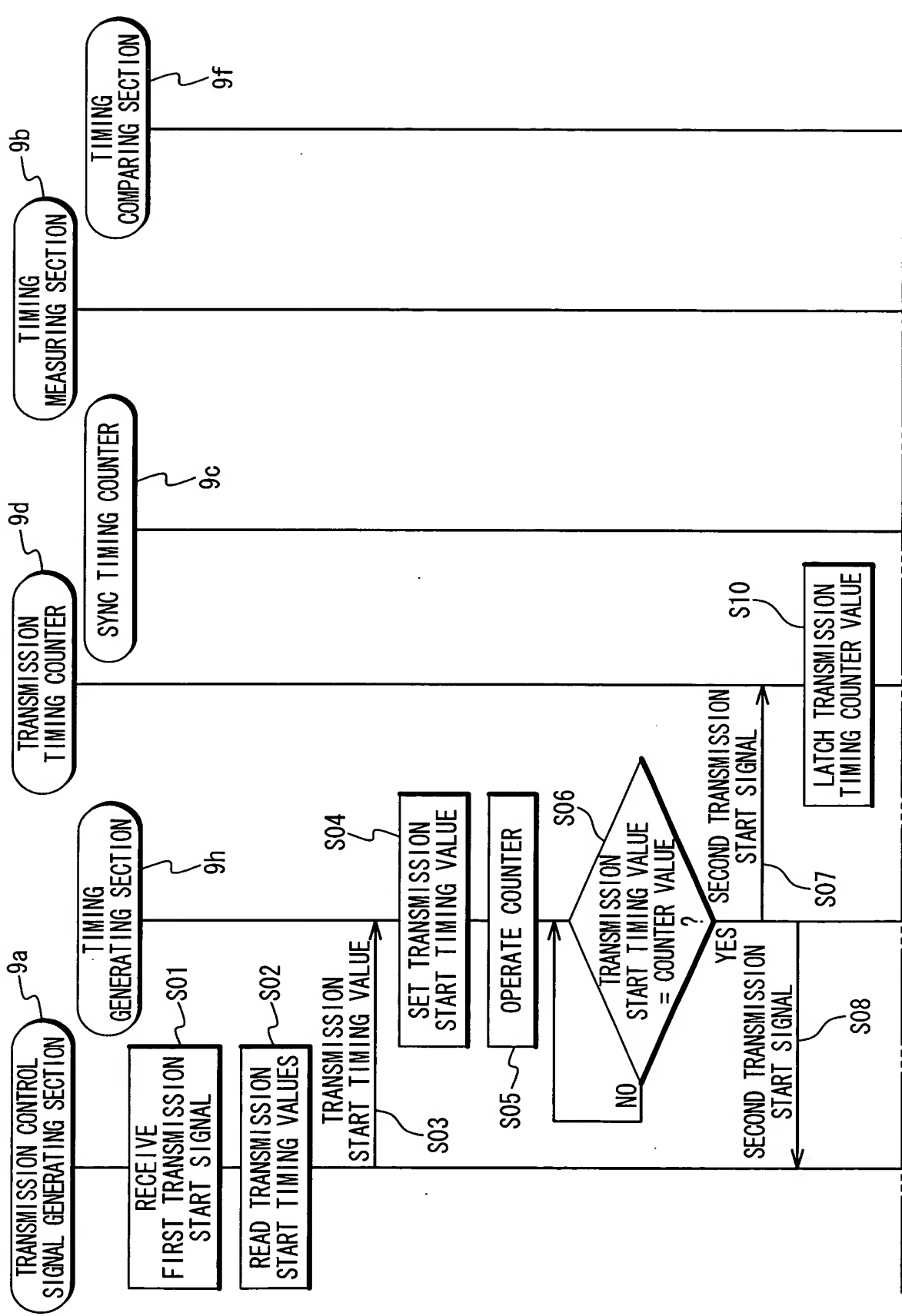


Fig. 5B

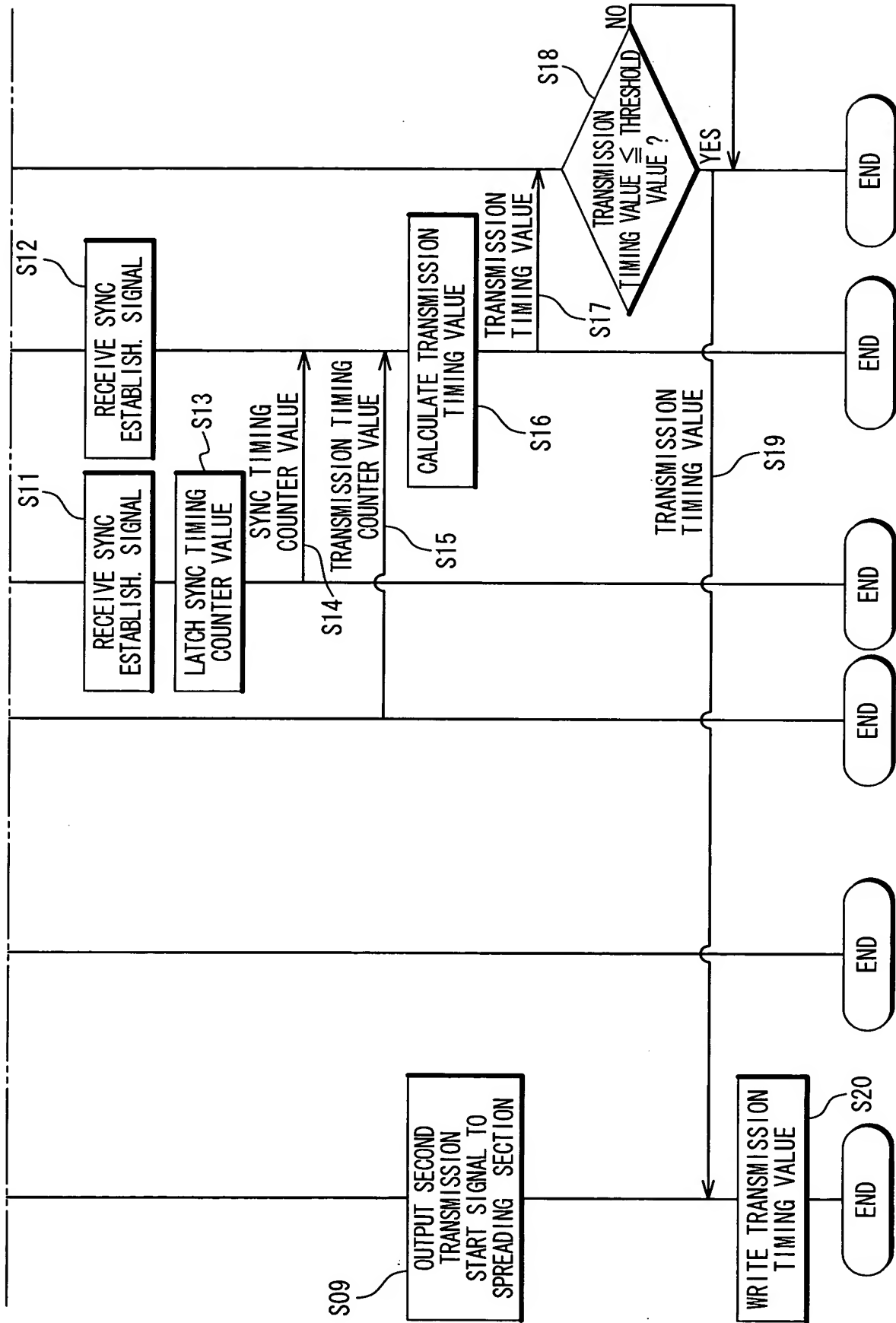


Fig. 6A



Fig. 6B

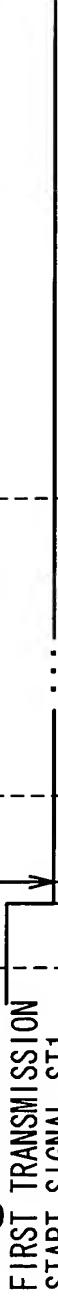


Fig. 6C

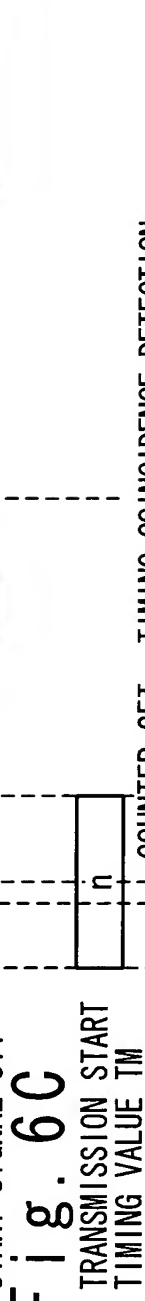


Fig. 6D



Fig. 6E

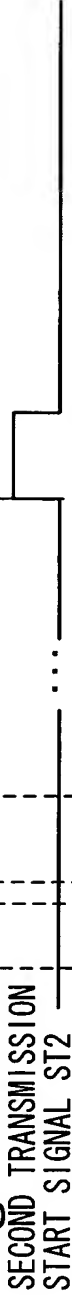


Fig. 6F



Fig. 6G



Fig. 6H



Fig. 7

